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CERTIFICATE OF MAILING

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No. : 10/600,941
Applicant : Jung-Chuh Tseng et al.
Title : METHOD AND DEVICE FOR GENERATING
SAMPLING SIGNAL
Filed : June 20, 2003
TC/A.U. : 2673
Examiner : (not yet assigned)
Docket No. : 3304.2.63
Customer No. : 21552

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

TRANSMITTAL OF SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Transmitted herewith is an Information Disclosure Statement disclosing information which has come to the attention of applicants and/or their attorneys and is being submitted so as to comply with the duty of disclosure set forth in 37 C.F.R. § 1.56. In accordance with 37 C.F.R. § 1.97(b), the enclosed Statement is being filed within three (3) months of the filing date of the above-identified application or before the mailing date of a first Action on the merits.

Neither applicants nor their attorneys make any representation that any information disclosed herein may be "prior art" within the meaning of that term under 35 U.S.C. § 102 or § 103. Moreover, pursuant to 37 C.F.R. § 1.97, the filing of this Information Disclosure Statement

shall not be construed as a representation that a search has been made or as an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b).

In accordance with 37 C.F.R. § 1.98, transmitted herewith are:

1. A completed copy of Form PTO/SB/08a "Information Disclosure Statement by Applicant" listing the patents, publications and other information being submitted for consideration; and
2. A legible copy of each patent, publication and other item of information in written form listed on the enclosed Form PTO/SB/08a, except for copies of U.S. patents and published U.S. patent applications which are not required for applications filed after June 30, 2003.

NON-ENGLISH INFORMATION

Pursuant to 37 C.F.R. § 1.98, following is a concise explanation of the relevance (as it is presently understood by the individual designated in 37 C.F.R. § 1.56(c) most knowledgeable about the content of the information), of each listed patent, publication or other information that is not in the English language:

1. Japanese Patent Publication No. 11-282426, published October 15, 1999 discloses: PROBLEM TO BE SOLVED: To provide the driving device of a liquid crystal device capable of surely preventing the degradation of display quality such as ghosts or the like by simple constitution even in the case of writing image signals line-sequentially for data lines. SOLUTION: A selection circuit 403 composed of 3-input NAND circuits and a buffer circuit 402 composed of inverters are provided between the respective output stages of a shift register 401 and a sampling circuit 301. The output signal line of the respective output stages of the shift register 401, the output signal line of the output stage adjacent to the output stage and an enable signal ENB outputted from an image control means are connected to the input terminal of the 3-input NAND circuit. Then, the enable signal ENB is a signal in common to all the 3-input NAND circuits, an output timing is within a period in which the output signals of the

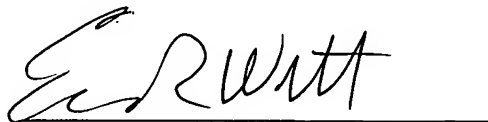
adjacent output stages are overlapped and a pulse width is necessarily shorter than the period.

2. Japanese Patent No. 2001215928, published August 10, 2001 discloses:
PROBLEM TO BE SOLVED: To prevent differential noises caused by enable signals ENB1, ENB2 from being superposed on a picture signal line 122.
SOLUTION: Two lines of inverted enable signal lines 125 to which inverted enable signals ENBinv, ENBinv are supplied are provided so that time constants of them become the same as those of enable signal lines 124 apart from two lines of the enable signal lines 124 to which enable signals ENB1, ENB2 are supplied. As a result, since differential noises due to the enable signals ENB1, ENB2 are canceled by differential noises due to the inverted enable signals ENBinv, ENBinv, the potential of a picture signal VIDI supplied to the picture signal line 122 becomes a value closed to an original value.

CERTIFICATION UNDER 37 C.F.R. § 1.97(e)

I hereby certify that each item of information listed on the enclosed Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement.

Respectfully submitted,



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Date: November 21, 2005

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PTO/SB/08a (08-03)
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary) Sheet 1 Of 1	Complete if Known	
	Application Number	10/600,941
	Filing Date	June 20, 2003
	First Named Inventor	Jung-Chuh Tseng
	Group Art Unit	2673
	Examiner Name	
	Attorney Docket Number	3304.2.63

FOREIGN PATENT DOCUMENTS							
Examiner Initials*	Cite No. ¹	Foreign Patent Document		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)					
	F1	JP – 2001215928		08/10/2001	Seiko Epson Corp.		
	F2	JP –11-282426		10/15/1999	Seiko Epson Corp.		

Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you are required to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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